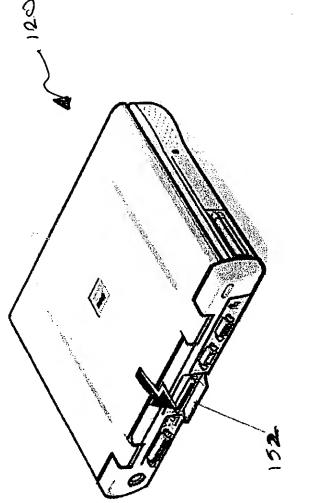
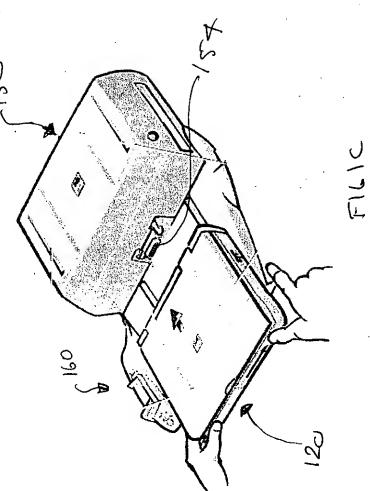
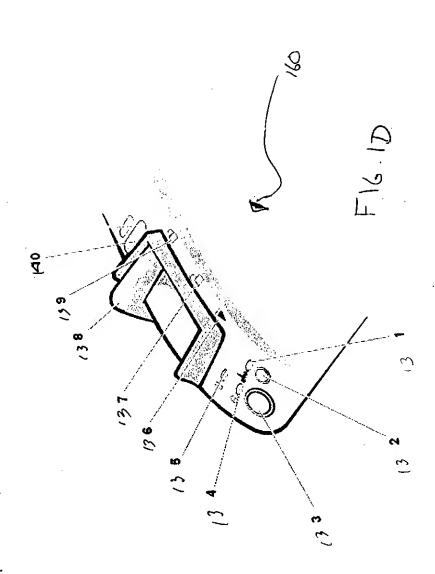


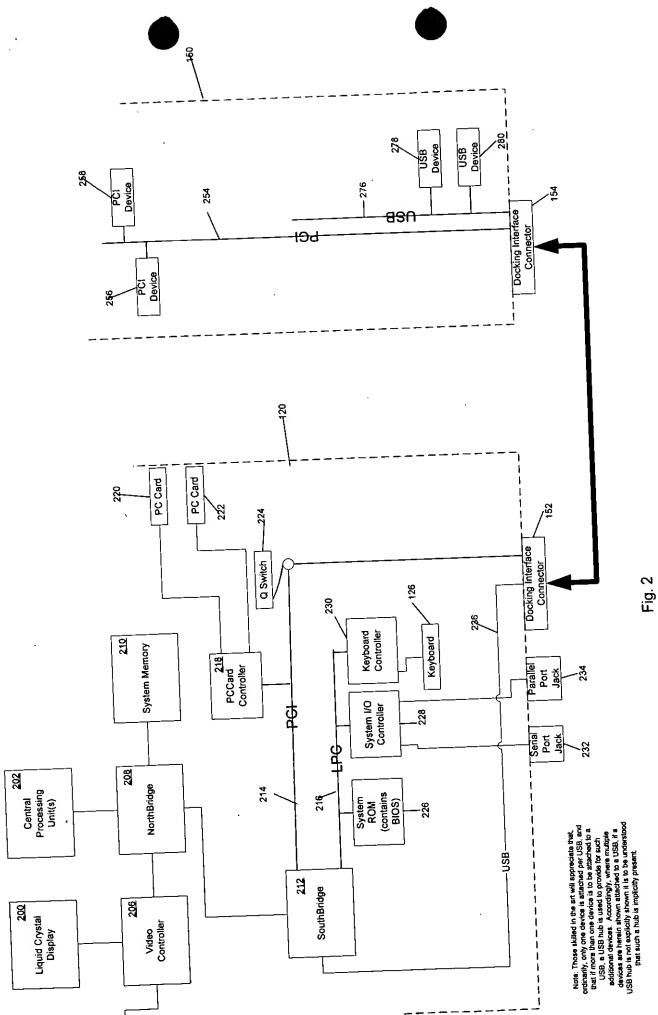
FIG. 1A

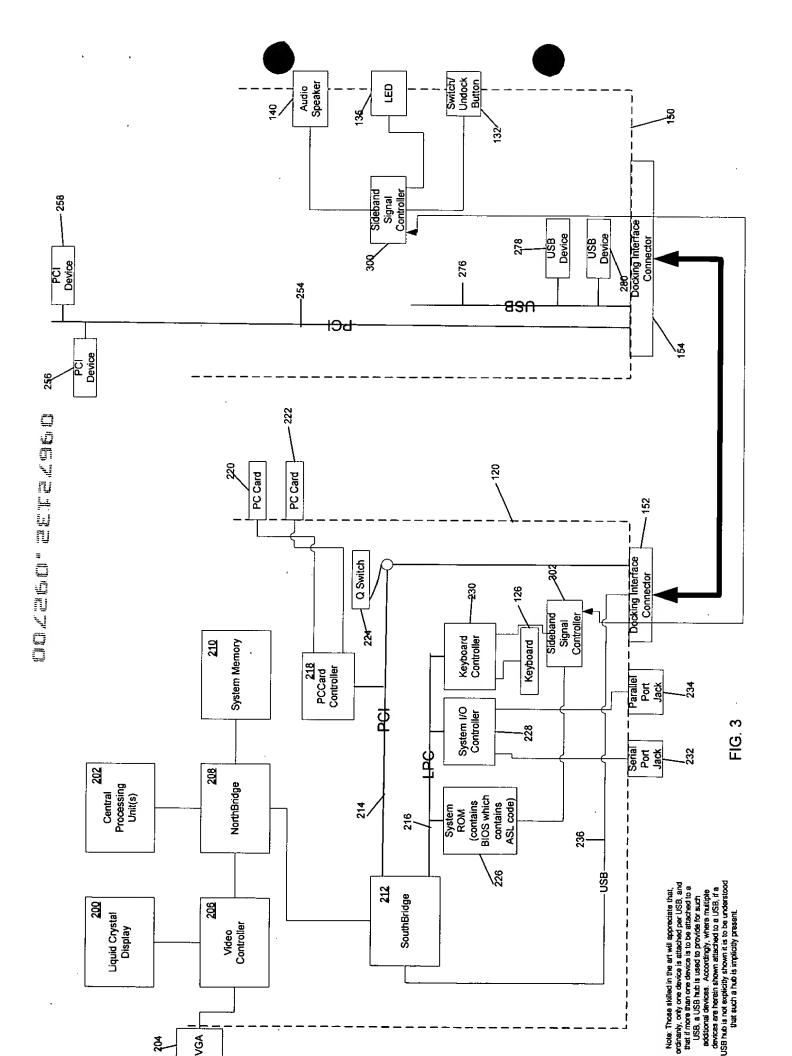


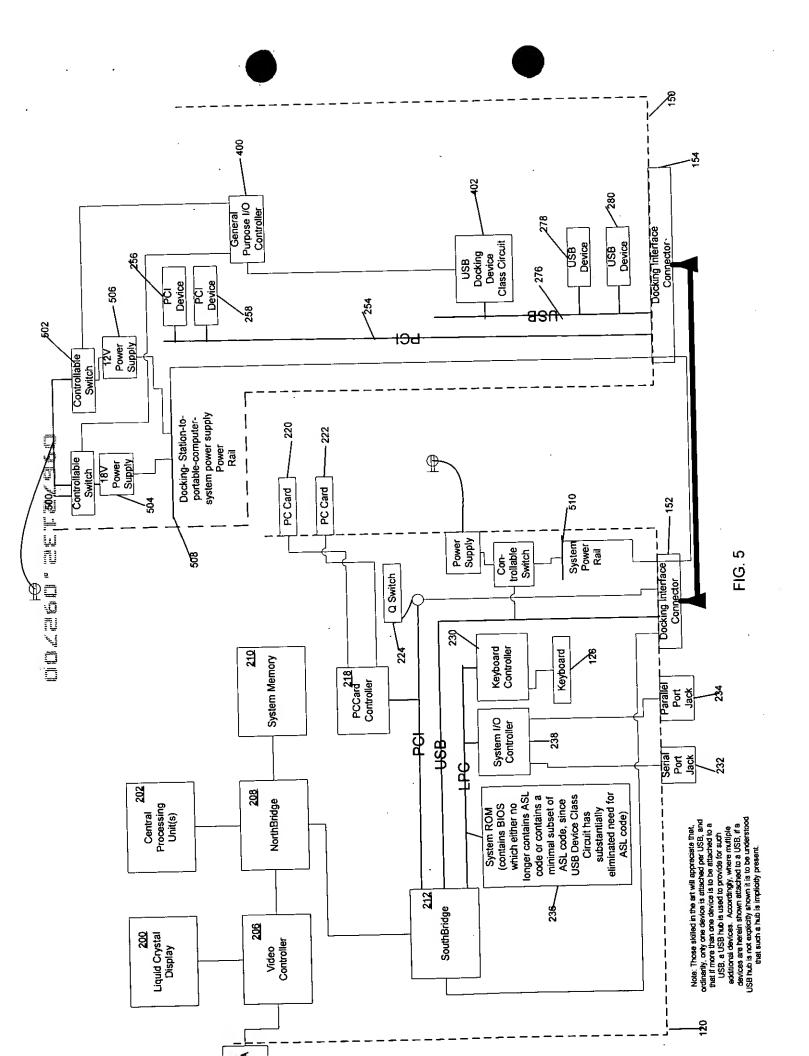
0191-

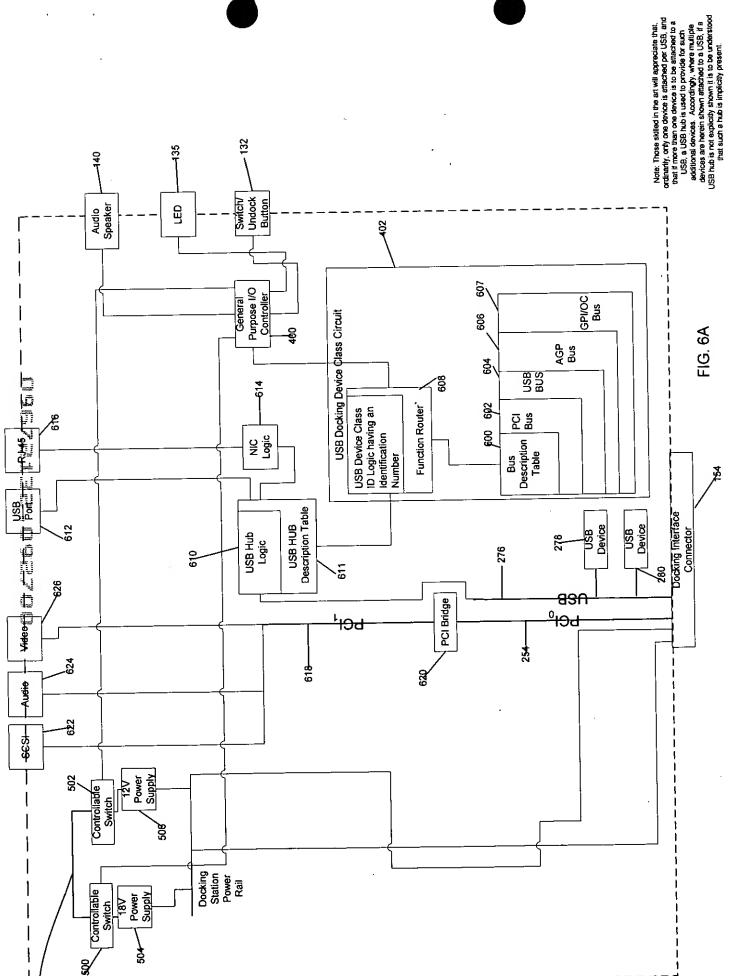


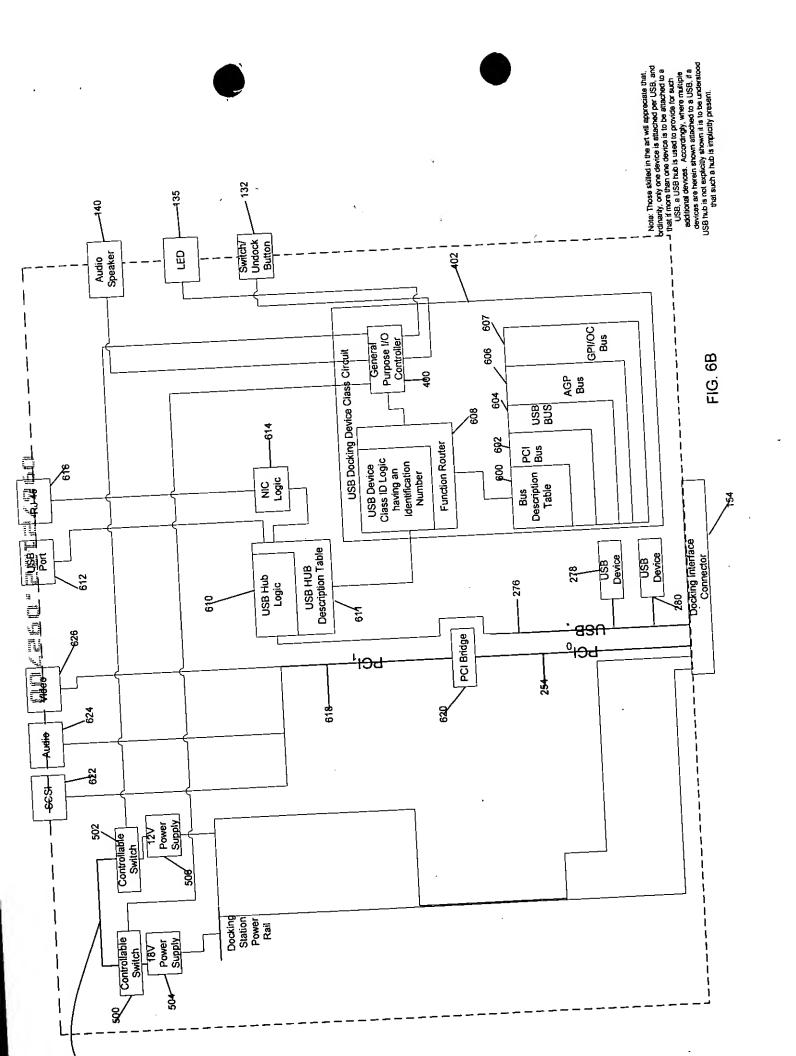












8 jure) are defined (e.g., USB, PCI, AT, etc.), and (2) which contains within it information which defines make up at least one subset of the universe of the can include ad hoc hardware and software within drive access LED and switch/undock button), the interfaced. The "universe" of the docking station which well known standards (either de facto or de docking station of which it (the docking device class circuit) forms a part, (3) which will thereby more data processing system components which the docking station (e.g., a docking-station harddocking station, and/or the power supply types The Docking Device Class Circuit is a device (1) defined functions associated with the bus with allow identification and/or control of the one or docking station via use of well known and wellbus structures and associated devices in the the at least one subset of the "universe" of a which will interface and function with a bus for and/or voltages within the docking station. which the docking device class circuit is Docking Device Class Circuit Docking Interface Connector Bus of known type, for which well-defined standards exist. For example, USB, PCI, AT, etc.

Fig. 7

ote: Those stilled in the art will appreciate that, frontly, only one device is attached per USB, and first if more than one device is to be attached to a USB at USB that is used to provide for each deticenel devices. Accordingly, where multiple svices are hearen shown attached to a USB, if a bub is not expicitly shown it is to be understood that auch a lyab is implicitly present.

